



04-19-04

PATENT  
040075

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Art Unit: :  
Examiner: :  
In re application of : THERMAL MANAGEMENT SYSTEM  
Durant et al. : AND METHOD  
Serial No.: 10/810,008 :  
Filed: March 26, 2004 :

INFORMATION DISCLOSURE STATEMENT

April 16, 2004

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants, in accordance with the duty of disclosure pursuant to 37 C.F.R. § 1.56, hereby advise the United States Patent and Trademark Office of the references listed on the accompanying Form PTO/SB/08 *Information Disclosure Statement by Applicant* (substitute for Form 1449A/PTO). A copy of each of the cited non-patent references is herewith enclosed.

Applicants note that although the cited references may be relevant to the examination of

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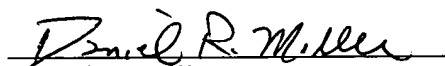
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the above-referenced application, under 37 C.F.R. § 1.97(h), the filing of this *Information Disclosure Statement by Applicant* "shall not be construed to be an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in § 1.56(b)."

Applicants further note that the filing of this *Information Disclosure Statement by Applicant* is not an admission that the references cited herein constitute prior art under 35 U.S.C. §§ 102-103 with respect to the captioned application.

Pursuant to 37 C.F.R. § 1.97(b)(3), Applicants submit that no fee is necessary for consideration of this *Information Disclosure Statement by Applicant* because an Office Action on the merits has not yet been mailed. Nevertheless, the Commissioner is hereby authorized to charge any additionally required fees necessary for consideration of this *Information Disclosure Statement by Applicant* to Account No. 11-1110.

Respectfully submitted,

  
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Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(use as many sheets as necessary)</i>				<b>Complete if Known</b>	
				Application Number	10/810,008
				Filing Date	March 26, 2004
				First Named Inventor	Durant
				Art Unit	
				Examiner Name	
Sheet	1	of	2	Attorney Docket Number	040075

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Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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PTO/SB/08B (10-01)

Approved for use through 10/31/2002. OMB 0651-0031

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		First Named Inventor	Durant		
		Art Unit			
		Examiner Name			
Sheet	2	of	2	Attorney Docket Number	040075

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
		Cruse, E., "An Integrated Thermal Architecture For Thermal Management Of High Power Electronics," Cooling Zone's Monthly Online Magazine, Vol. 3, No. 1, January, 2003, available on April 15, 2004 at <a href="http://www.coolingzone.com/Guest/News/NL_JAN_2003/Thermacore/Thermacore_Jan_2003.html">http://www.coolingzone.com/Guest/News/NL_JAN_2003/Thermacore/Thermacore_Jan_2003.html</a> .	
		"Enabling Intelligent Platform Management Interface (IPMI) Through Standard Building Blocks," Intel Corporation White Paper, June, 2002, available on April 15, 2004 at <a href="http://www.intel.com/platforms/applied/elacomm/papers/25133701.pdf">http://www.intel.com/platforms/applied/elacomm/papers/25133701.pdf</a> .	
		"FFB 30 x 80 x38 MM Series," Delta Electronics, Inc. Online Catalog, pg. 32, 2002-2003, available on April 15, 2004 at <a href="http://www.delta.com.tw/products/dcfans/pdf/FFB808038.pdf">http://www.delta.com.tw/products/dcfans/pdf/FFB808038.pdf</a> .	
		Hanrahan, D., "Fan-Speed Control Techniques In PCs," Analog Dialogue, Vol. 34, No. 4, June-July, 2000, available on April 15, 2004 at <a href="http://www.analog.com/library/analogDialogue/archives/34-04/fan/">http://www.analog.com/library/analogDialogue/archives/34-04/fan/</a>	
		"IPMI v1.5 Overview," Intel Corporation White Paper, 2001, available on April 15, 2004 at <a href="http://developer.intel.com/design/servers/IPMI15Whitepaper.pdf">http://developer.intel.com/design/servers/IPMI15Whitepaper.pdf</a> .	
		Kundert, J., "For Optimal Cooling, Rely On Closed-Loop, Fan-Speed Control," Electronic Design, July 6, 1998, available on April 15, 2004 at <a href="http://www.findarticles.com/cf_dls/m3161/n16_v46/20929121/print.html">http://www.findarticles.com/cf_dls/m3161/n16_v46/20929121/print.html</a> .	
		P. Horowitz and W. Hill, "The Art of Electronics," Cambridge University Press, 1980, 1989, pg. 49.	
		Paparrizos, G., "An Integrated Fan Speed Control Solution Can Lower System Costs, Reduce Acoustic Noise, Power Consumption and Enhance System Reliability," February 12, 2003, Microchip Technology, Inc. Application Note, available on April 15, 2004 at <a href="http://www.microchip.com/download/appnote/thermal/tb063a.pdf">www.microchip.com/download/appnote/thermal/tb063a.pdf</a> .	
		"PIC16F87X Datasheet," Microchip Technology Inc. Product Datasheet, pgs. 57-110, January 30, 2001, available on April 15, 2004 at <a href="http://www.microchip.com/download/lit/pline/picmicro/families/16f87x/30292c.pdf">http://www.microchip.com/download/lit/pline/picmicro/families/16f87x/30292c.pdf</a> .	
		Stancil, C., "Keeping PCs Cool And Quiet," Compaq Computer Corporation," available on April 15, 2004 at <a href="http://www.robbrownstein.com/assets/Keeping%20PCs%20Cool%20And%20Quiet.pdf">http://www.robbrownstein.com/assets/Keeping%20PCs%20Cool%20And%20Quiet.pdf</a> .	

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